AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1-24. (Cancelled)

25. (Previously Presented) An LCD panel comprising:

an insulating substrate defined as a cell region and a pad region, the pad region including data pads;

a plurality of gate lines formed in the cell region; and

a plurality of data lines crossing the gate lines and having alternately different extended lengths to the data pads, wherein alternately different extended lengths are also respectively at different levels above the insulating substrate, the data pads applying an electric signal to the data lines.

26. (Currently Amended) ~~A liquid crystal display(LCD) panel comprising:~~

~~a substrate having a cell region and a pad region;~~

~~a plurality of gate and data lines formed in the cell region,~~ The LCD panel of claim 25, wherein the plurality of data lines includes a first set of data lines and a second set of data lines formed in the cell region, ~~wherein the plurality of gate lines is arranged to cross the plurality of data lines to form a plurality of pixel regions within the cell region;~~ and

wherein the data pads includes:

a first set of data pads formed in the pad region, wherein each data pad in the first set of data pads extends from a corresponding one of the first set of data lines; and

a second set of data pads formed in the pad region, wherein the first and second set of data pads are arranged in a single-bank structure in the pad region, wherein each data pad in the second set of data pads is electrically connected to a corresponding one of the second set of data lines, and wherein the first and the second set of data pads are arranged so that a plane substantially perpendicular to the substrate and substantially parallel to a data line in the first set of data lines contacts a data pad in the first set of data pads and a data pad in the second set of data pads.

27. (Previously Presented) The LCD panel of claim 26, wherein the first set of data lines includes all odd- numbered data lines in the plurality of data lines, and wherein the second set of data lines includes all even-numbered data lines in the plurality of data lines.

28. (Previously Presented) The LCD panel of claim 26, wherein each data pad in the second set of data pads includes:

a conductive pattern having a first end in the cell region and a second end in the pad region, wherein the longitudinal axis of the second end corresponds with that of the adjacent one of the first set of data lines, wherein the conductive pattern is formed on the substrate at a level below that of an adjacent one of the first set of data pads and wherein the first end is connected to the corresponding one of the second set of data lines; and

a first conductive film connected to the second end of the conductive pattern.

29. (Previously Presented) The LCD panel of claim 28, wherein the conductive pattern is formed in one of the following parts on the substrate;

a first part where at least a portion of the conductive pattern falls perpendicularly below the adjacent one of the first set of data pads; and

a second part where no portion of the conductive pattern falls perpendicularly below the adjacent one of the first set of data pads.

30. (Previously Presented) The LCD panel of claim 29, wherein the conductive pattern in the second part is formed on one or more sides of the adjacent one of the first set of data pads.

31. (Previously Presented) The LCD panel of claim 28, wherein the conductive pattern includes a refracted portion linking the first and the second ends thereof.

32. (Previously Presented) The LCD panel of claim 28, wherein the conductive pattern is of the same material as that of the plurality of gate lines.

33. (Previously Presented) The LCD panel of claim 28, further comprising an insulating layer interposed between the conductive pattern and the adjacent one of the first set of data pads located above the conductive pattern.

34. (Previously Presented) The LCD panel of claim 28, further comprising a second conductive film connecting the first end of the conductive pattern to the corresponding one of the second set of data lines.

35. (Previously Presented) The LCD panel of claim 28, wherein the conductive pattern has the same width as that of the corresponding one of the second set of data lines.

36. (Previously Presented) The LCD panel of claim 26, wherein a spacing between two consecutive data pads in the first set of data pads is at least two times that between two consecutive data lines in the plurality of data lines.

37. (Previously Presented) The LCD panel of claim 26, further comprising a plurality of thin film transistors, wherein each thin film transistor is formed at a crossing point between a corresponding gate line from the plurality of gate lines and a corresponding data line from the plurality of data lines.

38. (Previously Presented) The LCD panel of claim 26, further comprising a plurality of pixel electrodes, wherein each pixel electrode is formed in a corresponding pixel region from the plurality of pixel regions.

39. (Previously Presented) The LCD panel of claim 26, wherein each data line in the second set of data lines comprises the following:

an extension portion formed a predetermined distance into the pad region; and

a refracted portion formed in the pad region, wherein the refracted portion commencing at an end of the extension portion in the pad region and terminating as a corresponding data pad in the second set of data pads.

40. (Previously Presented) The LCD panel of claim 39, wherein the end of the extension portion in the pad region is coextensive with an adjacent data pad from the first set of data pads.

41. (Previously Presented) The LCD panel of claim 26, wherein the first and second sets of data pads are arranged so that the plane substantially perpendicular to the substrate coincides with a data line in the first set of data lines.